

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	METHOD, SYSTEM AND APPARATUS FOR AGGREGATING FAILURES ACROSS MULTIPLE MEMORIES AND APPLYING A COMMON DEFECT REPAIR SOLUTION TO ALL OF THE MULTIPLE MEMORIES
--------------------	---

Application Number :

Confirmation Number:

First Named Applicant: Michael Richard Ouellette

Attorney Docket Number: BUR920030042US1

Art Unit:

Examiner:

Search string: (4333142 or 4333142).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4333142	1982-06- 01	Gilman D. Chesley			
	2	5222066	1993-06- 22	Jerome A. Grula et al.			
	3	5388104	1995-02- 07	Tsukasa Shirotori et al.			
	4	5475815	1995-12- 12	Larry L. Byers et al.			
	5	5535164	1996-07- 09	Robert D. Adams et al.			
	6	5617531	1997-04- 01	Alfred L. Crouch et al.			
	7	5835502	1998-11- 10	Anthony Gus Aipperspach et al			
	8	5974579	1999-10- 26	Yervant David Lepejian et al.			
	9	5995731	1999-11- 30	Alfred Larry Crouch et al.			
	10	6085346	2000-07- 04	Yervant David Lepejian et al.			
	11	6360342	2002-03- 19	Kuen-Jong Lee			

				et al.			
--	--	--	--	--------	--	--	--

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20020046377	2002-04- 18	Oliver Kniffler et al.			
	2	20020174394	2002-11- 21	James S. Ledford et al.			
	3	20030016578	2003-01- 23	Thomas Janik et al.			

Signature

Examiner Name	Date